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DATE: April 26, 2005

PTO IDENTIFIER: Application Number 10/730,892-Conf. #5181
Patent Number

Inventor: Katherine L. Saenger et al.

MESSAGE TO: US Patent and Trademark Office

FAX NUMBER: (703) 872-9306

FROM: CONNOLLY BOVE LODGE & HUTZ LLP

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Attorney Dkt. #: 20140-00316-US

PAGES (Including Cover Sheet): 3

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Response to Restriction Requirement (without Traverse) (1 page)
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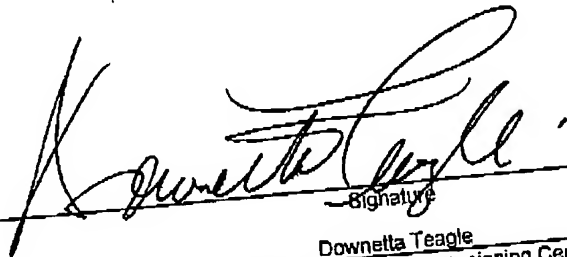
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Response to Restriction Requirement (without Traverse) (1 page)

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Docket No.: 20140-00316-US
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Katherine L. Saenger et al.

Application No.: 10/730,892

Filed: December 10, 2003

For: FIELD EFFECT TRANSISTOR WITH
ETCHED-BACK GATE DIELECTRIC

Confirmation No.: 5181

Art Unit: 2815

Examiner: M. C. Landau

RESPONSE TO RESTRICTION REQUIREMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the restriction requirement set forth in the Office Action mailed March 30, 2005, Applicant hereby provisionally elects claims 1-16 for continued examination.

The Examiner has required restriction between claims 1-32 because, according to the Examiner, two inventions have been presented.

Applicant believes no fee is due with this response. However, if a fee is due, please charge our Deposit Account No. 50-0510, under Order No. 20140-00316-US from which the undersigned is authorized to draw.

Dated: April 26, 2005

Respectfully submitted,

By

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